

Assistant/Associate Professor in Microelectronics Integrated Circuit Design for the IoT/5G Revolution

FACULTY POSITION

“Groupe HEI-ISA-ISEN” Graduate Schools of Engineering at the Catholic University of Lille invites applications for a full-time tenure track position (starting in or before September 2016) in Electrical Engineering at Assistant or Associate Professor Level and affiliated to ISEN Graduate School of Engineering in Lille (<http://www.isen.fr/lille/>).

ABOUT Groupe HEI-ISA-ISEN GRADUATE SCHOOL OF ENGINEERING

- Groupe HEI-ISA-ISEN comprises 3 Schools of Engineering at the Catholic University of Lille and offers 18 fields of specialization at the master level including interdisciplinary programs with emphasis on management and innovation skills.
- Engineering programs & diplomas of Groupe HEI-ISA-ISEN are accredited by the French « Commission des Titres Ingénieur ». Groupe HEI-ISA-ISEN is an active member of the “Conférence des Grandes Ecoles”
- Groupe HEI-ISA-ISEN is highly active in Research in Science and Engineering with excellent international reputation, accredited by the French CNRS (National Center for Scientific Research).

JOB QUALIFICATIONS

Keywords: IoT, Microelectronics, Integrated Circuit Design, Communication systems, RF transceivers, low power circuits.

Teaching Profile:

Attached to ISEN engineering school, the candidate will be part of the education program in electrical engineering and microelectronics. More specifically, the candidate will be in charge of setting up and maintaining courses, in one or more of the following domains depending on the profile: analog and digital electronics, microcontrollers, FPGA, DSP, real-time embedded systems. This includes lectures and hands-on labs, as well as coordinating the modules if it integrates teaching assistants and/or external lecturers. The candidate will also be involved in other modules in the electrical engineering department. Finally, the candidate will be active in student project monitoring, student internship visits and school promotion through internal and external events.

Research Profile:

The candidate will be part of the Integrated Circuit Design Research Group of the SMART department at ISEN – IEMN. This group mainly works on advanced CMOS IC design for communication systems, dealing with aspects from system-level down to transistor-level. The group focuses on digital, mixed-signal and analog/RF/mmW aspects in integrated circuit design. To reinforce the competences in the area of low-power circuits for the **Internet of Things revolution**, the group is seeking candidates with a strong expertise and validated background in either (1) **low-power integrated circuits**, (2) **IC design for communicating objects**, (3) **integrated sensors and data conversion** or any field related to integrated circuit design. The

candidate will be in charge of building research-oriented projects within the group, following project development on technical and administrative aspects, coordinating Master, PhD students and post-docs on group-related topics, as well as being involved in the group environment life.

The candidate should hold a PhD and should have demonstrated successful implementations and measurements of state-of-the-art integrated circuits as well as an ability for leadership. The candidate should master the use of design flows and kits using Cadence Virtuoso suite. Use of ADS, EM simulation tools, Matlab, VHDL/Verilog-AMS and all related software is preferable.

Applicants should be completely fluent in English as part of the courses will be taught in this language.

SALARY RANGE

The salary is competitive. Employees receive French social security benefits, complementary health insurance and a contributory pension scheme.

APPLICATION PROCEDURE

The application package should consist of a cover letter, curriculum vitae, full list of publications, contact details for two referees and student teaching evaluations when possible. A short research plan (2 pages max), with highlights on the candidate profile and adequacy with the group activity, is preferable for the selection process.

All applications and inquiries should be submitted by e-mail directly to Prof. Antoine Frappé (antoine.frappe@isen-lille.fr). Applications should be sent with copy to the human resource department (recrutement@isa-lille.fr)

APPLICATIONS WILL BE REVIEWED UPON RECEPTION AND WILL CONTINUE UNTIL POSITION IS FILLED.